

<b>Notice of References Cited</b>	Application/Control No. 09/989,777	Applicant(s)/Patent Under Reexamination NEMECEK, CRAIG	
	Examiner Ayal I. Sharon	Art Unit 2123	Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,466,898	10-2002	Chan, Terence	703/17
	B	US-6,618,854	09-2003	Mann, Daniel Peter	717/124
	C	US-6,058,263	05-2000	Voth, David W.	703/25
	D	US-5,911,059	06-1999	Profit, Jr., Jack H.	703/23
	E	US-5,572,665	11-1996	Nakabayashi, Takeo	714/28
	F	US-2002/0156885	10-2002	Thakkar, Bina Kunal	709/224
	G	US-2002/0116168	08-2002	KIM, MAHN BOK	703/28
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Harrison, C.G. et al. "Xilinx FPGA Design in a Group Environment Using VHDL and Synthesis Tools". Colloquim on Digital System Design Using Synthesis Techniques. Feb 15, 1996. pp.5/1-5/4.
	V	
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.